## **PATENT APPLICATION**



## THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yasushi YAMASAKI et al. Group Art Unit: 2811

Application No.: 09/970,766 Examiner: S. Hu

Filed: October 5, 2001 Docket No.: 110791

For: METHOD OF MANUFACTURING SEMICONDUCTOR SUBSTRATE,

SEMICONDUCTOR SUBSTRATE, ELECTRO-OPTICAL APPARATUS AND

**ELECTRONIC EQUIPMENT** 

## SUPPLEMENTAL AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

As a supplement to the Amendment filed on <u>July 21, 2004</u>, please further consider the following:

Amendments to the Claims as reflected in the listing of claims;

Remarks.